



SPN6338

Dual N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN6338 is the Dual N-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

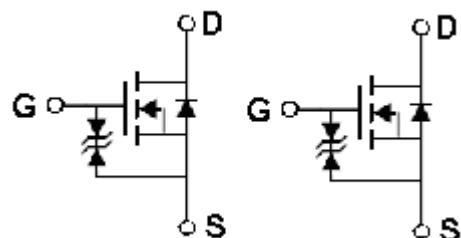
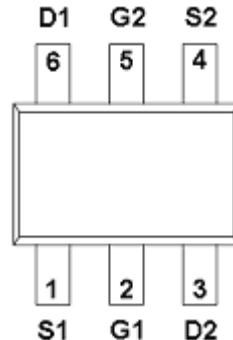
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

FEATURES

- ◆ N-Channel
30V/0.95A,RDS(ON)=550mΩ@VGS=4.5V
30V/0.75A,RDS(ON)=650mΩ@VGS=2.5V
30V/0.65A,RDS(ON)=850mΩ@VGS=1.8V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-363 (SC-70-6L) package design

PIN CONFIGURATION(SOT-363 / SC-70-6L)



PART MARKING



Y : Year Code
W: Week Code



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PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	D2	Drain 2
4	S2	Source 2
5	G2	Gate 2
6	D1	Drain1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN6338S36RGB	SOT-363	38YW

※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)

※ SPN6338S36RGB : Tape Reel ; Pb – Free ; Halogen – Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	VDSS	30	V
Gate – Source Voltage	VGSS	±12	V
Continuous Drain Current(TJ=150°C)	TA=25°C	1.2	A
	TA=80°C		
Pulsed Drain Current	IDM	4	A
Continuous Source Current(Diode Conduction)	IS	0.6	A
Power Dissipation	TA=25°C	0.35	W
	TA=70°C		
Operating Junction Temperature	TJ	-55/150	°C
Storage Temperature Range	TSTG	-55/150	°C



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ELECTRICAL CHARACTERISTICS

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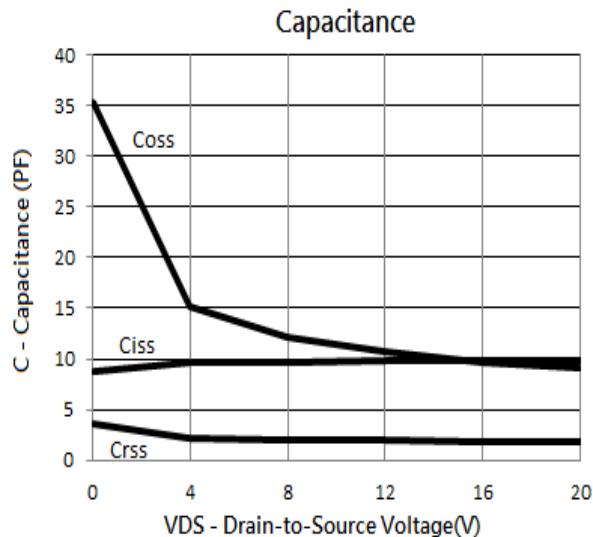
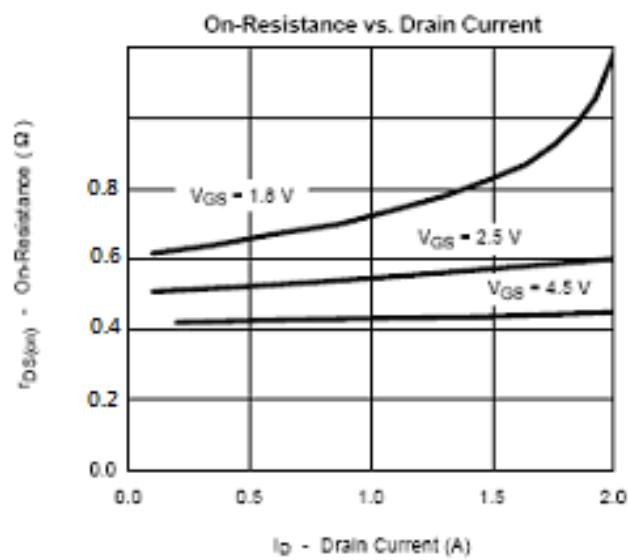
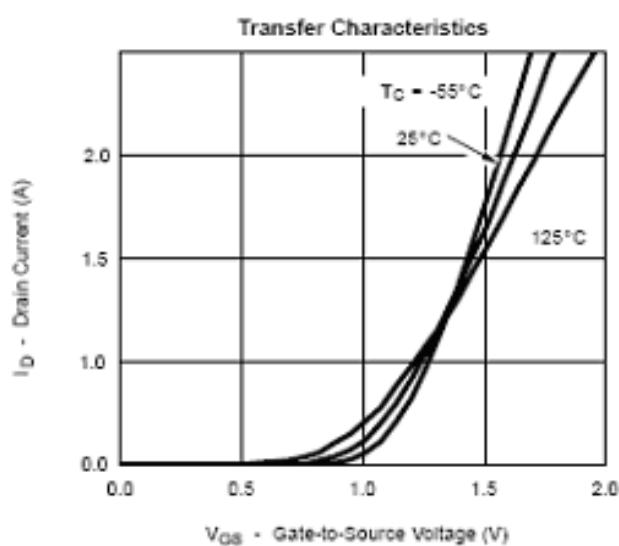
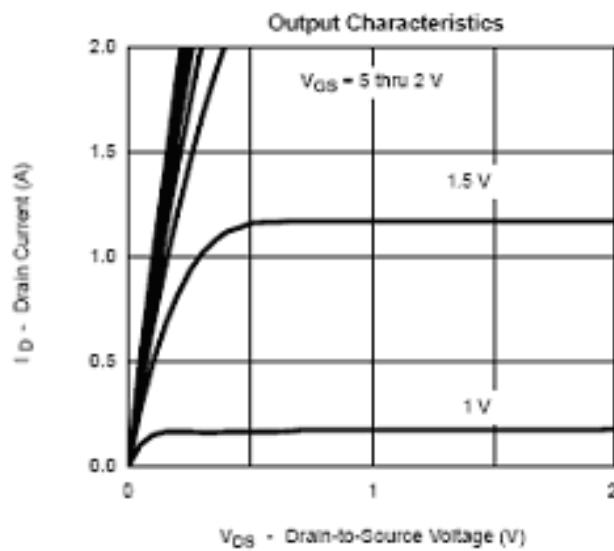
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID= 250uA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=250uA	0.35		1.0	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			30	uA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} =0V			1	uA
		V _{DS} = 24V, V _{GS} =0V T _J =55°C			5	
On-State Drain Current	I _{D(on)}	V _{DS} ≥ 4.5V, V _{GS} =5V	0.7			A
Drain-Source On-Resistance	R _{D(on)}	V _{GS} =4.5V, ID=0.95A		0.45	0.55	Ω
		V _{GS} =2.5V, ID=0.75A		0.50	0.65	
		V _{GS} =1.8V, ID=0.65A		0.70	0.85	
Forward Transconductance	g _{fs}	V _{DS} =10V, ID=0.4A		1.0		S
Diode Forward Voltage	V _{SD}	I _S =0.15A, V _{GS} =0V		0.8	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =10V, V _{GS} =4.5V, ID=0.6A		1.2	1.5	nC
Gate-Source Charge	Q _{gs}			0.2		
Gate-Drain Charge	Q _{gd}			0.3		
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0V f=1MHz		7.2		pF
Output Capacitance	C _{oss}			17		
Reverse Transfer Capacitance	C _{rss}			1.6		
Turn-On Time	t _{d(on)}	V _{DD} =10V, R _L =10Ω , ID=0.5A V _{GEN} =4.5V , R _G =6Ω		5	10	ns
	t _r			8	15	
Turn-Off Time	t _{d(off)}			10	18	
	t _f			1.2	2.8	



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TYPICAL CHARACTERISTICS

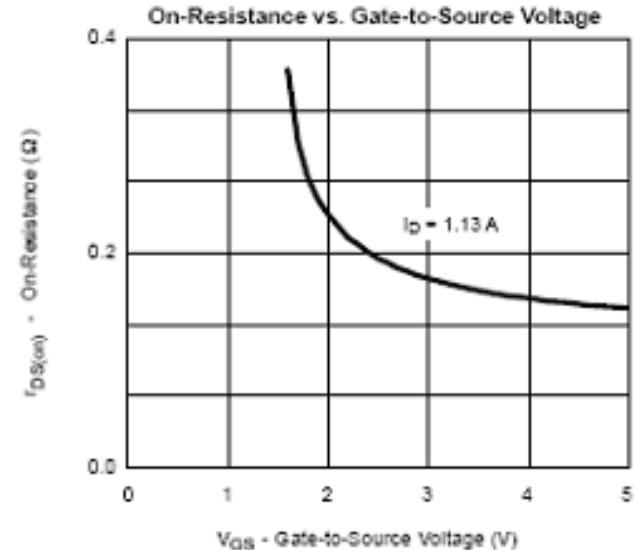
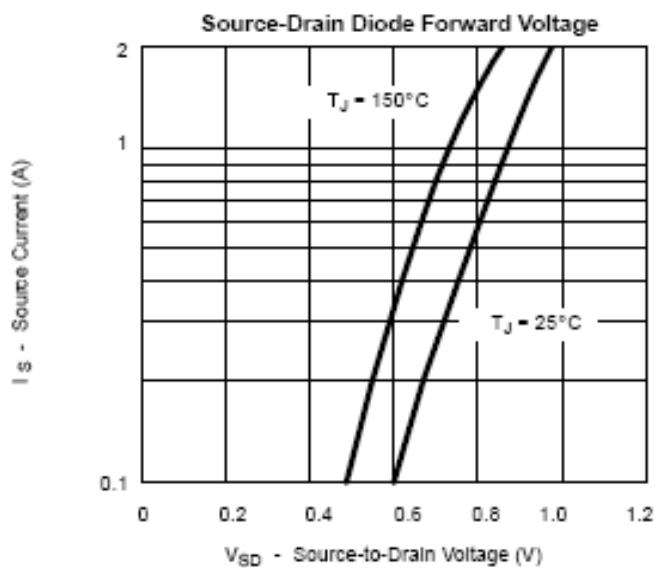
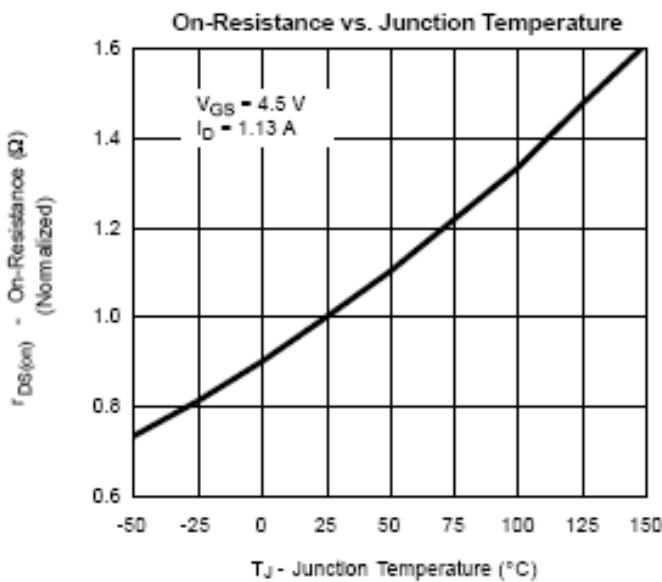
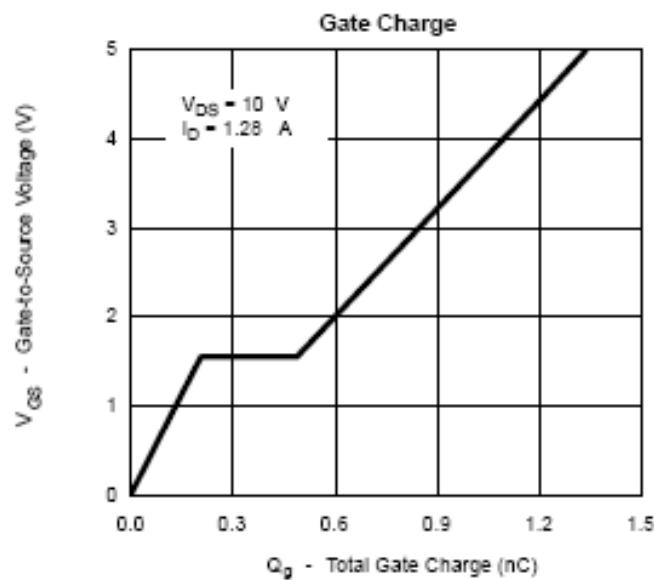




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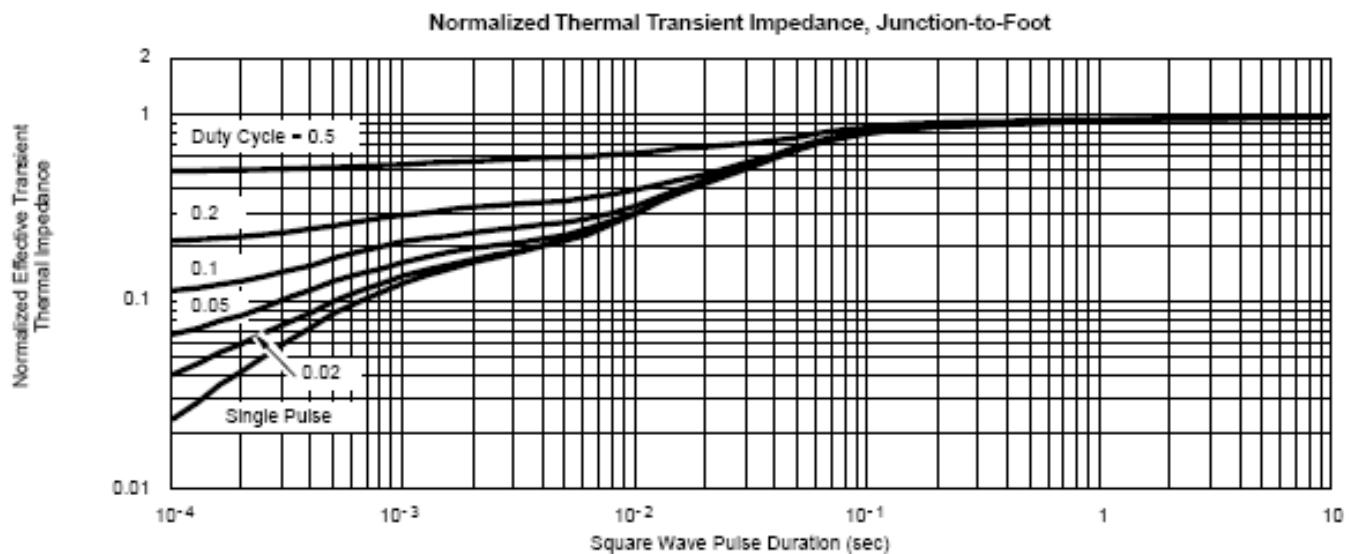
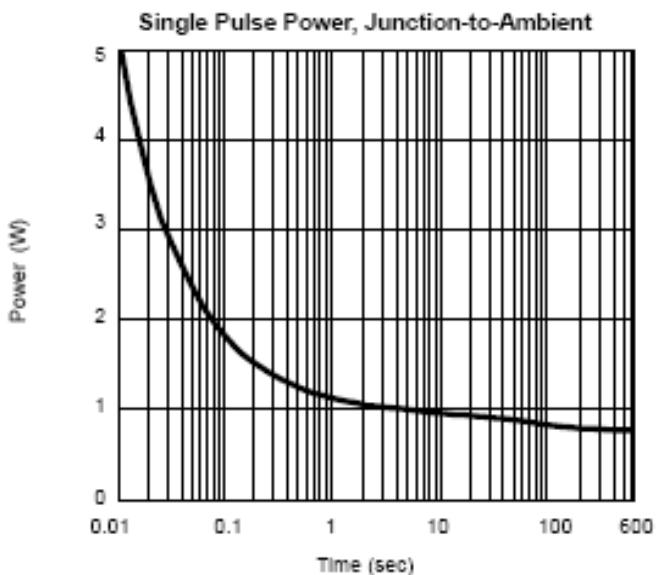
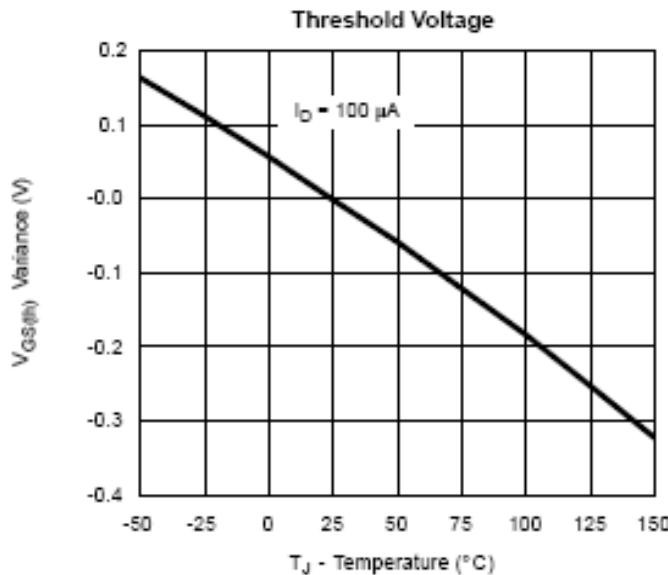




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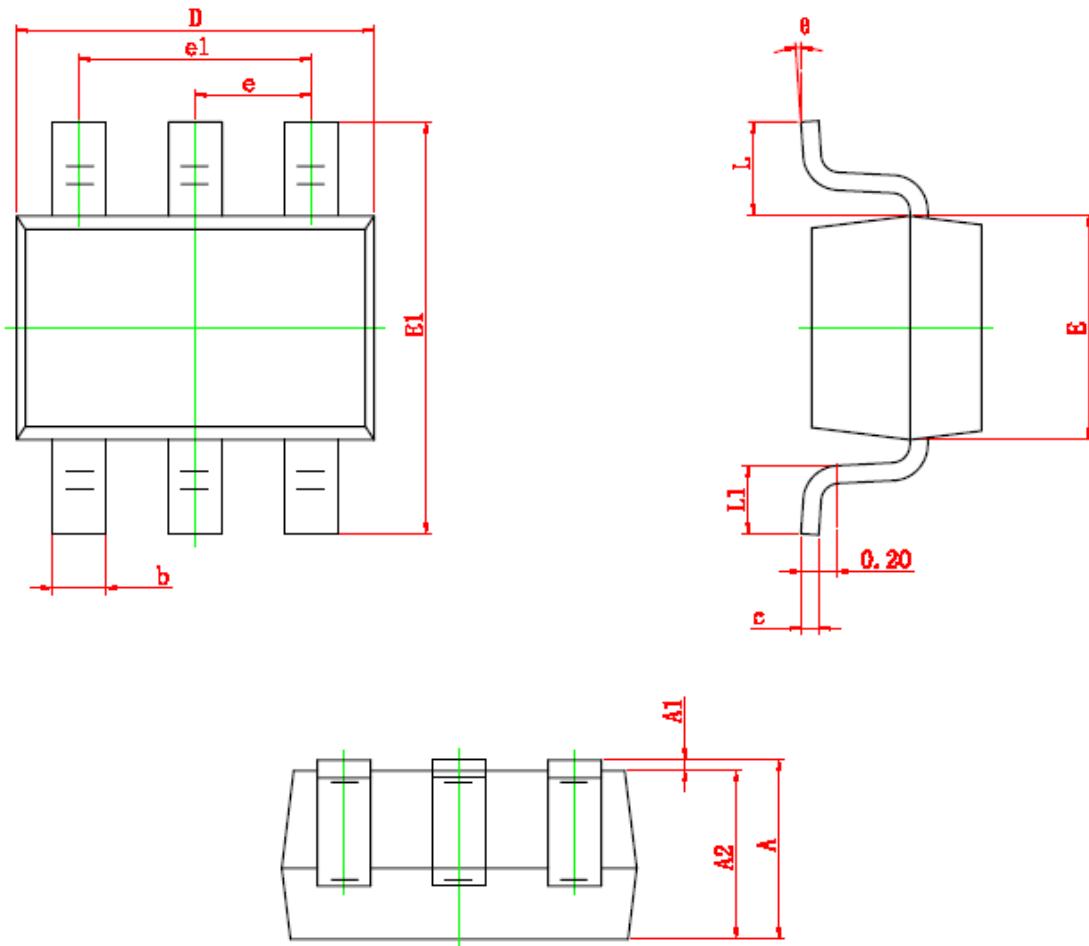




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SOT-363 PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°